



1.5A 2MHz 5.5V Synchronous Buck Converter

Description

The ME3161 is a high-efficiency, DC-to-DC step-down switching regulators, capable of delivering up to 1.5A of output current. The device operates from an input voltage range of 2.6V to 5.5V and provides an output voltage from 0.6V to V_{IN} . Working at a fixed frequency of 2MHz allows the use of small external components, such as ceramic input and output caps, as well as small inductors, while still providing low output ripples. This low noise output along with its excellent efficiency achieved by the internal synchronous rectifier, making ME3161 an ideal replacement for large power consuming linear regulators. Internal soft-start control circuitry reduces inrush current. Short-circuit and thermal shutdown protection improves design reliability.

The ME3161 is available in SOT23-5 and DFN1.6x1.6-6 packages

Applications

- Cellular phones
- Digital cameras
- MP3 and MP4 players
- Set top boxes
- Wireless and DSL modems
- USB supplied devices in notebooks
- Portable devices

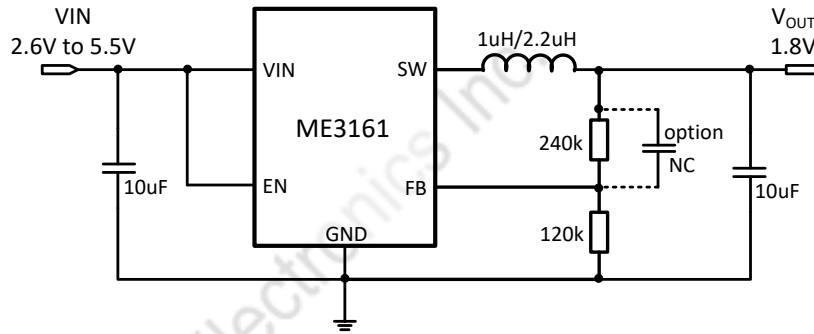
Feature

- High efficiency: up to 97%
- Up to 1.5A Max output current
- 2MHz switching frequency
- Low dropout 100% duty operation
- Internal compensation and soft-start
- Current mode control
- Reference 0.6V
- Logic control shutdown ($I_{Q} < 1\mu A$)
- Thermal shutdown, UVLO

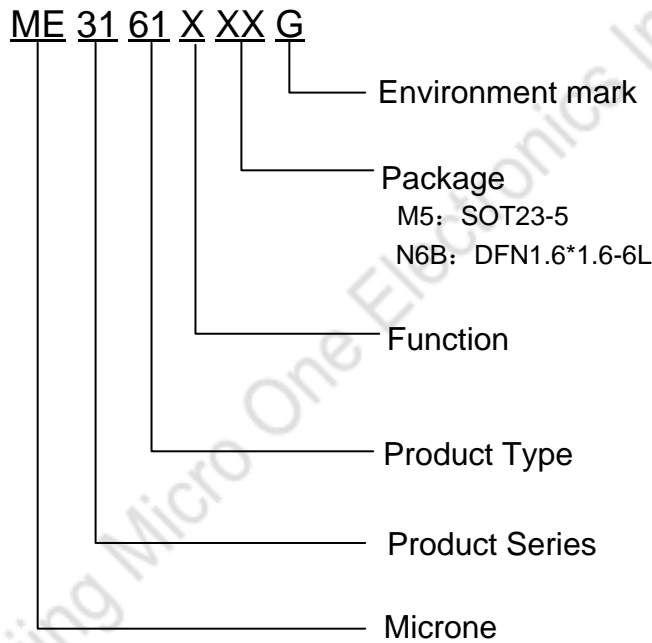
Package

- 5-pin SOT23-5
- 6-pin DFN1.6*1.6-6L

Typical Application Circuit



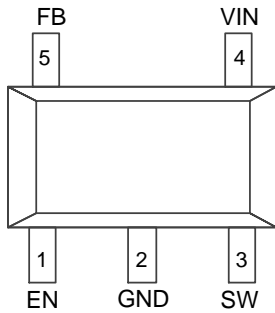
Selection Guide



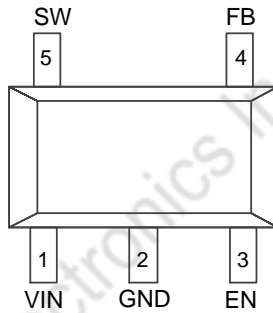
product series	product description
ME3161AM5G	Package: SOT23-5
ME3161BM5G	Package: SOT23-5(Different pin)
ME3161CN6BG	Package: DFN1.6*1.6-6L

NOTE: If you need other voltage and package, please contact our sales staff.

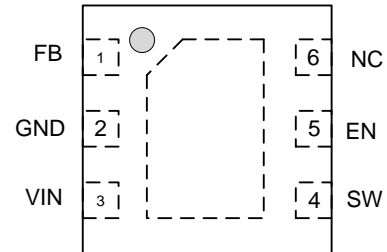
Pin Configuration



SOT23-5



SOT23-5(B)



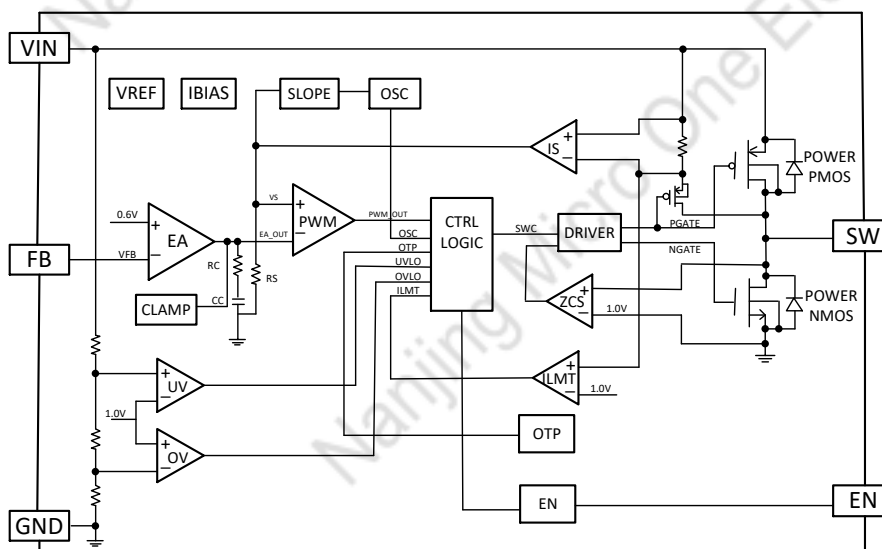
DFN1.6*1.6-6L

Pin Assignment

PIN Number SOT23-5	PIN Number SOT23-5(B)	PIN Number DFN1.6*1.6-6L	symbol	Function
1	3	5	EN	Enable pin for the IC. Drive the pin to high to enable the part, and low to disable
2	2	2	GND	Ground
3	5	4	SW	Inductor connection. Connect an inductor between SW and the regulator output.
4	1	3	VIN	Supply voltage.
5	4	1	FB	Feedback input. Connect an external resistor divider from the output to FB and GND to set the output to a voltage between 0.6V and Vin
-	-	6	NC	No connection

Note: Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.

Block Diagram



Absolute Maximum Ratings($T_A=25^{\circ}\text{C}$)

Parameter		Value	Units
Max input voltage		8	V
Max operating junction temperature(T_J)		125	$^{\circ}\text{C}$
Ambient temperature(T_A)		-40– 85	$^{\circ}\text{C}$
Maximum power dissipation	SOT23-5	400	mW
Maximum power dissipation	DFN1.6x1.6-6L	600	mW
Package thermal resistance (θ_{JA})		125	$^{\circ}\text{C}/\text{W}$
Package thermal resistance (θ_{JC})		30	$^{\circ}\text{C}/\text{W}$
Storage temperature(T_S)		-40- 150	$^{\circ}\text{C}$
Lead temperature & time		260 $^{\circ}\text{C}$, 10S	
ESD (HBM)		>2000V	

Electrical Characteristic

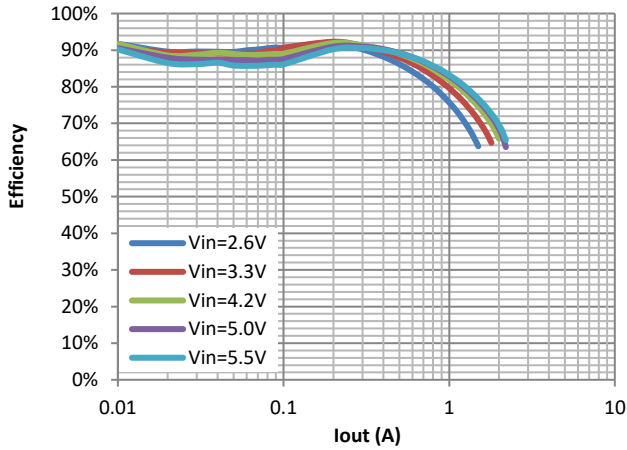
ME3161 test conditions : $V_{IN}=5V$, $T_A=25\text{ }^{\circ}C$, unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{IN}	Input voltage range		2.6		5.5	V
V_{OVP}	Input overvoltage threshold			6.1	6.5	V
V_{REF}	Feedback voltage	$V_{IN}=5V$	0.588	0.6	0.612	V
I_Q	Quiescent current	Active, $V_{FB}=0.65$, No Switching		55		μA
I_{FB}	Feedback leakage current			0.1	1	μA
$I_{SHUTDOWN}$	Shutdown input current	$EN=0V$			1	μA
I_{SWLK}	SW leakage current	$V_{IN}=6V$, $V_{SW}=0$ or $6V$, $EN=0V$			1	μA
I_{ENLK}	EN leakage current				1	μA
LNR	Line regulation	$V_{IN}=2.6V$ to $5.5V$		0.1	0.2	%/V
LDR	Load regulation	$I_{OUT}=0.01$ to $1A$		0.1	0.2	%/A
F_{SOC}	Switching frequency			2		MHz
R_{DSON_P}	PMOS R_{dson}			250	350	$m\Omega$
R_{DSON_N}	NMOS R_{dson}			150	250	$m\Omega$
V_{UVLO}	Under voltage lockout		1.9	2.1	2.3	V
V_{UVLO_HY}	UVLO hysteresis			100		mV
I_{LIMIT}	Peak current limit			2.3		A
V_{H_EN}	EN input high voltage		1.2			V
V_{L_EN}	EN input low voltage				0.5	V
T_{SD}	Thermal shutdown temp			160		$^{\circ}C$
T_{SH}	Thermal shutdown hysteresis			15		$^{\circ}C$

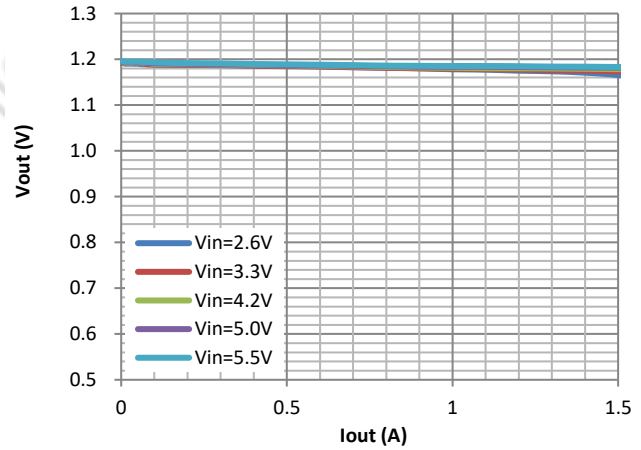
Typical Performance Characteristics

Tested under $T_A=25^\circ\text{C}$, unless otherwise specified

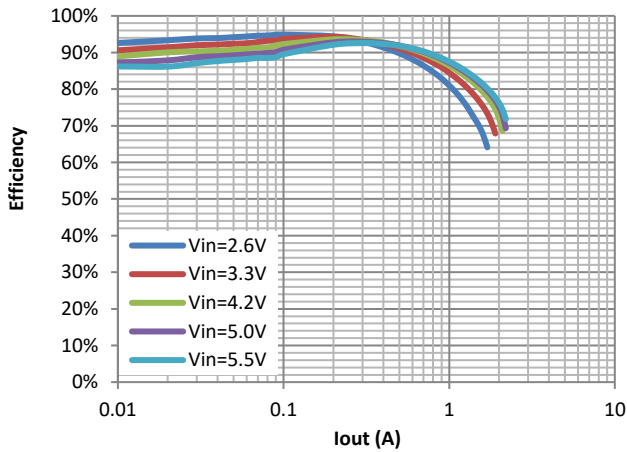
Efficiency vs. Output Current
($V_{out}=1.2\text{V}$)



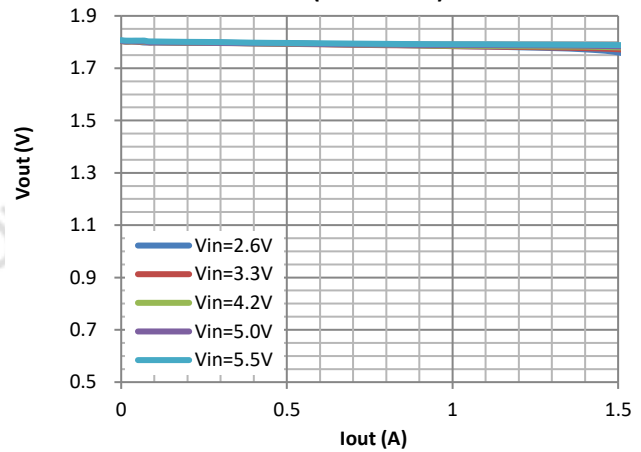
Load Regulation
($V_{out}=1.2\text{V}$)



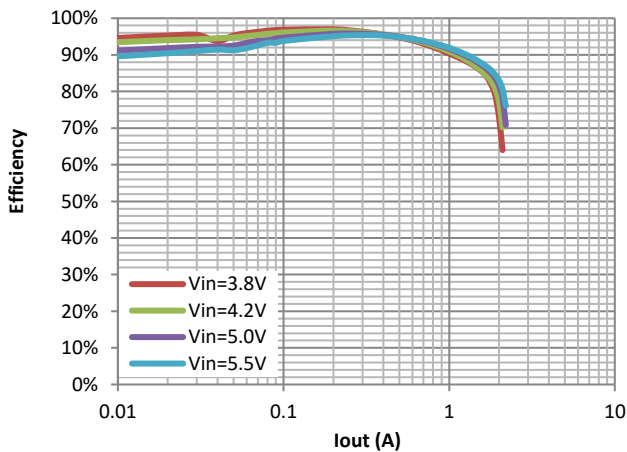
Efficiency vs. Output Current
($V_{out}=1.8\text{V}$)



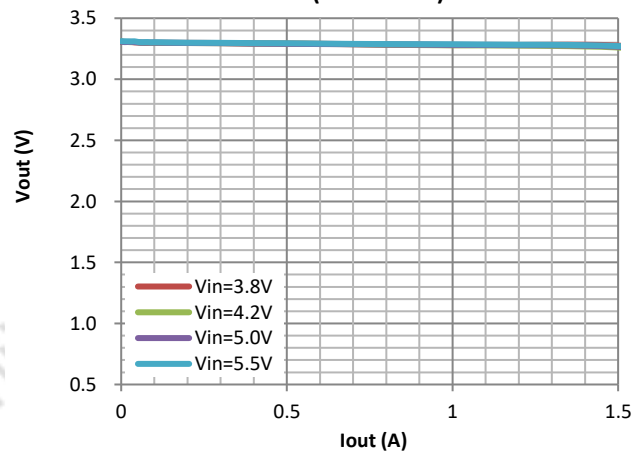
Load Regulation
($V_{out}=1.8\text{V}$)



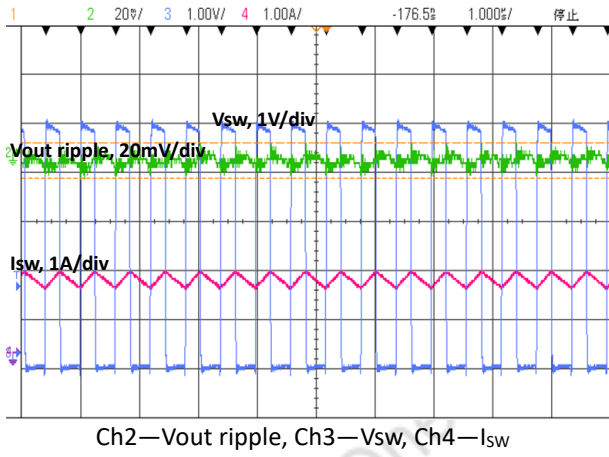
Efficiency vs. Output Current
($V_{out}=3.3\text{V}$)



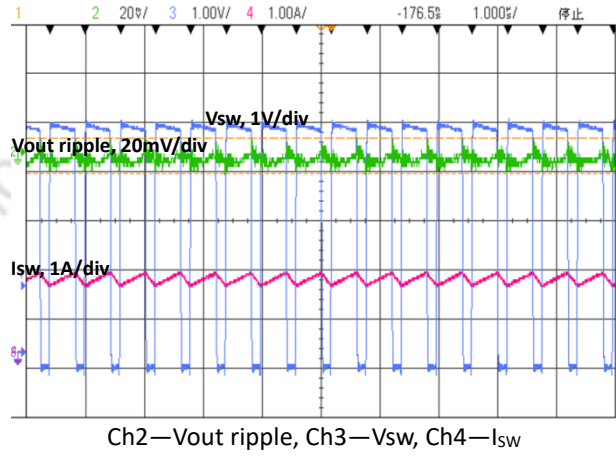
Load Regulation
($V_{out}=3.3\text{V}$)



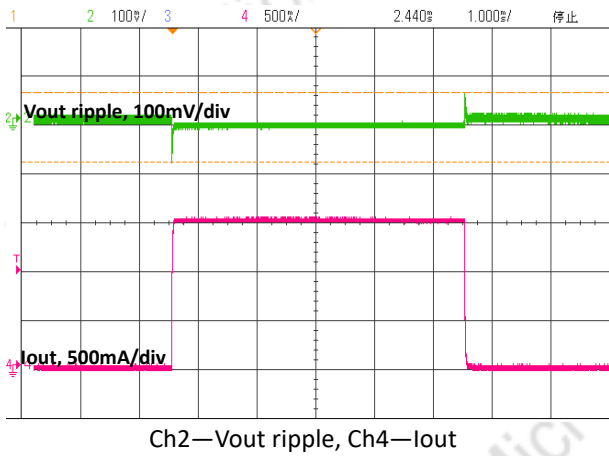
Output Ripple and SW at 1.5A load
Vin=3.3V / Vout=1.8V



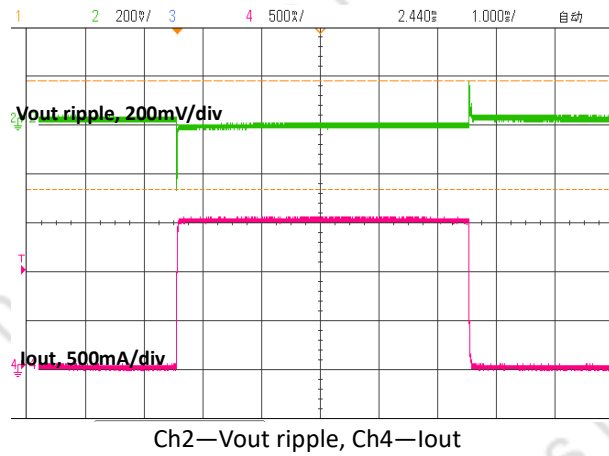
Output Ripple and SW at 1.5A load
Vin=5V / Vout=3.3V



Load Transient
Vin=5V / Vout=1.2V / Iout=0.01~1.5A



Load Transient
Vin=5V / Vout=3.3V / Iout=0.01~1.5A



Operation Principles

The ME3161 high-efficiency switching regulator is a small, simple, DC-to-DC step-down converter capable of delivering up to 1.5A of output current. The device operates in pulse-width modulation (PWM) at 2MHz from a 2.6V to 5.5V input voltage and provides an output voltage from 0.6V to V_{IN} , making the ME3161 ideal for on-board post-regulation applications. An internal synchronous rectifier improves efficiency and eliminates the typical Schottky free-wheeling diode. Using the on resistance of the internal high-side MOSFET to sense switching currents eliminates current-sense resistors, further improving efficiency and cost.

Loop operation

ME3161 uses a PWM current-mode control scheme. An open-loop comparator compares the integrated voltage-feedback signal against the sum of the amplified current-sense signal and the slope compensation ramp. At each rising edge of the internal clock, the internal high-side MOSFET turns on until the PWM comparator terminates the on cycle. During this on-time, current ramps up through the inductor, sourcing current to the output and storing energy in the inductor. The current mode feedback system regulates the peak inductor current as a function of the output voltage error signal. During the off cycle, the internal high-side P-channel MOSFET turns off, and the internal low-side N-channel MOSFET turns on. The inductor releases the stored energy as its current ramps down while still providing current to the output.

Current sense

An internal current-sense amplifier senses the current through the high-side MOSFET during on time and produces a proportional current signal, which is used to sum with the slope compensation signal. The summed signal then is compared with the error amplifier output by the PWM comparator to terminate the on cycle.

Current limit

There is a cycle-by-cycle current limit on the high-side MOSFET of 2.3A (typ). When the current flowing out of SW exceeds this limit, the high-side MOSFET turns off and the synchronous rectifier turns on. ME3161 utilizes a frequency fold-back mode to prevent overheating during short-circuit output conditions. The device enters frequency fold-back mode when the FB voltage drops below 100mV, limiting the current to 2.3A (typ) and reducing power dissipation. Normal operation resumes upon removal of the short-circuit condition.

Soft-start

ME3161 has an internal soft-start circuitry to reduce supply inrush current during startup conditions. When the device exits under-voltage lockout (UVLO), shutdown mode, or restarts following a thermal shutdown event, the soft-start circuitry slowly ramps up current available at SW.

UVLO

If VIN drops below 2.1V, the UVLO circuit inhibits switching. Once VIN rises above 2.2V, the UVLO clears, and the soft-start sequence activates.

Thermal shutdown

Thermal shutdown protection limits total power dissipation in the device. When the junction temperature exceeds $T_J = +160^\circ\text{C}$, a thermal sensor forces the device into shutdown, allowing the die to cool. The thermal sensor turns the device on again after the junction temperature cools by 15°C , resulting in a pulsed output during continuous overload conditions. Following a thermal-shutdown condition, the soft-start sequence begins.

DESIGN PROCEDURE

Setting output voltages

Output voltages are set by external resistors. The FB threshold is 0.6V.

$$R_{\text{TOP}} = R_{\text{BOTTOM}} \times \left(\frac{V_{\text{OUT}}}{0.6} - 1 \right)$$

Input capacitor selection

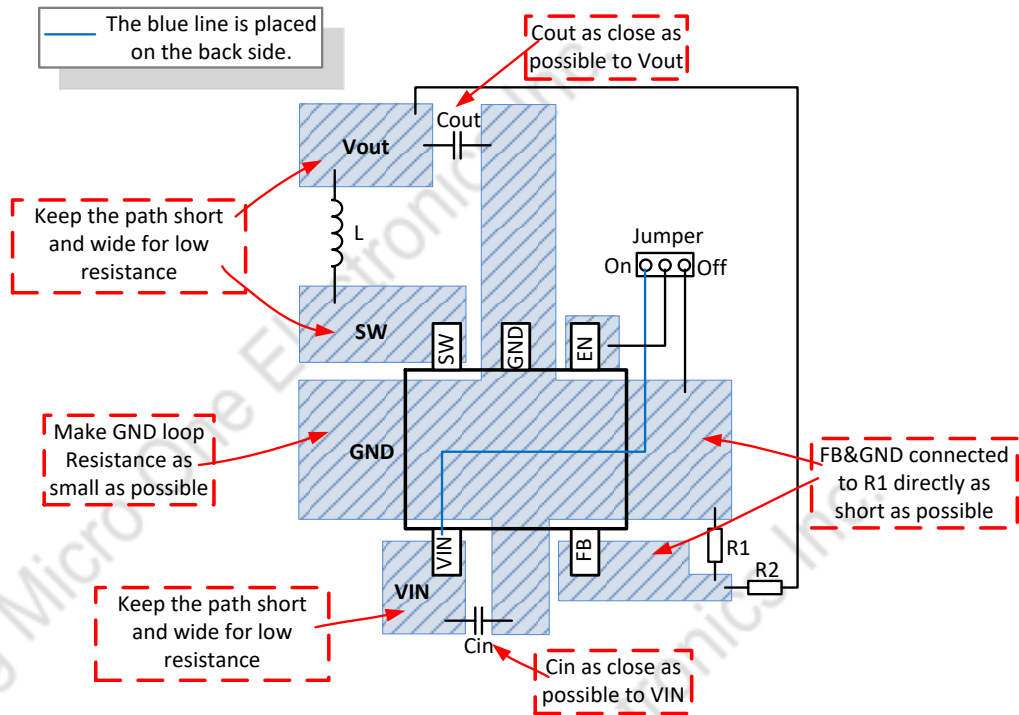
The input capacitor in a DC-to-DC converter reduces current peaks drawn from the battery or other input power source and reduces switching noise in the controller. The impedance of the input capacitor at the switching frequency should be less than that of the input source so high-frequency switching currents do not pass through the input source. The output capacitor keeps output ripple small and ensures control-loop stability. The output capacitor must also have low impedance at the switching frequency. Ceramic, polymer, and tantalum capacitors are suitable, with ceramic exhibiting the lowest ESR and high-frequency impedance. Output ripple with a ceramic output capacitor is approximately as follows:

$$\Delta I_L = \frac{V_{\text{OUT}}}{L \times f_S} \times \left(1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}} \right)$$
$$\Delta V_{\text{OUT}} = \frac{V_{\text{OUT}}}{8 \times f_S^2 \times L \times C_{\text{OUT}}} \times \left(1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}} \right)$$

If the capacitor has significant ESR, the output ripple component due to capacitor ESR is as follows:

$$\Delta V_{\text{OUT}} = \frac{V_{\text{OUT}}}{f_S \times L} \times \left(1 - \frac{V_{\text{OUT}}}{V_{\text{IN}}} \right) \times R_{\text{ESR}}$$

LAYOUT GUIDE

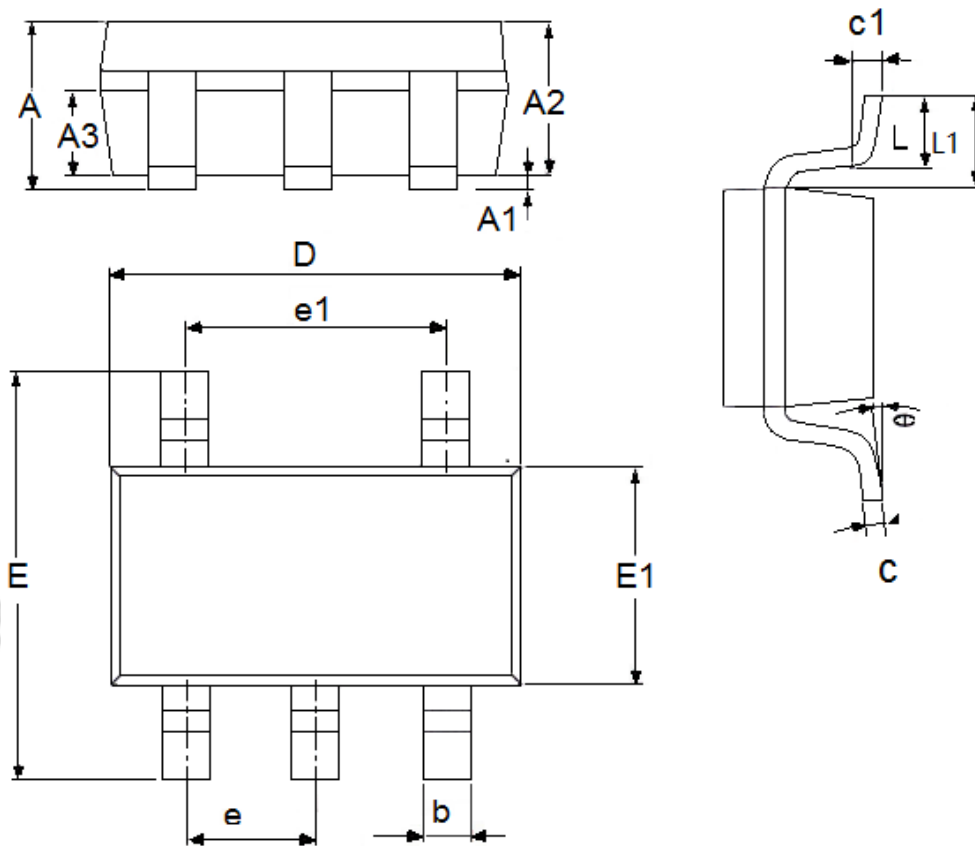


Package Quantity

Package Type	Minimum Packing QTY	UNITS	Small Box	Large BOX
SOT23-5	3000	Tape & Reel	30K	120K
DFN1.6*1.6-6L	3000	Tape & Reel	30K	120K

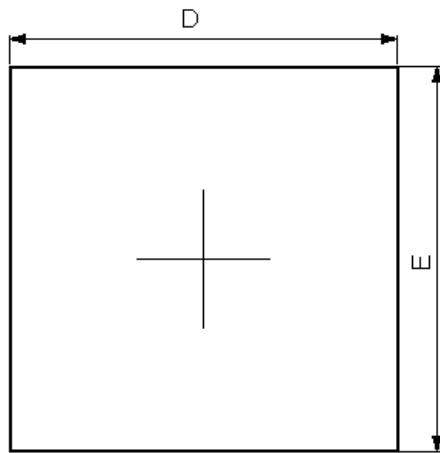
Package Information

- Package Type:SOT23-5

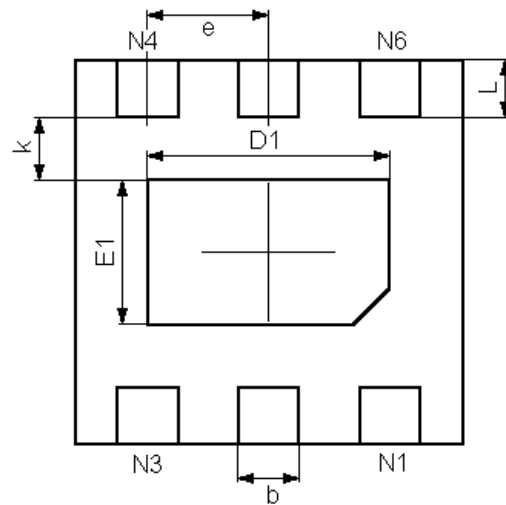


DIM	Millimeters		Inches	
	Min	Max	Min	Max
A	1.05	1.45	0.0413	0.0571
A1	0	0.15	0.0000	0.0059
A2	0.9	1.3	0.0354	0.0512
A3	0.6	0.7	0.0236	0.0276
b	0.25	0.5	0.0098	0.0197
c	0.1	0.23	0.0039	0.0091
D	2.82	3.05	0.1110	0.1201
e1	1.9(TYP)		0.0748(TYP)	
E	2.6	3.05	0.1024	0.1201
E1	1.5	1.75	0.0512	0.0689
e	0.95(TYP)		0.0374(TYP)	
L	0.3	0.6	0.0118	0.0236
L1	0.59(TYP)		0.0232(TYP)	
theta	0	8°	0.0000	8°
c1	0.2(TYP)		0.0079(TYP)	

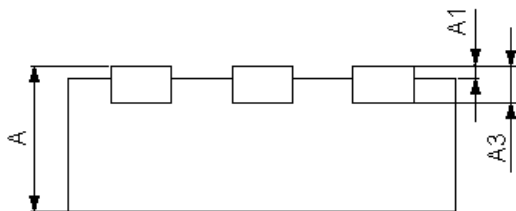
- 封装类型: DFN1.6*1.6-6L



TOP VIEW



BOTTOM VIEW



SIDE VIEW

DIM	Millimeters		Inches	
	Min	Max	Min	Max
A	0.5	0.6	0.0197	0.0236
A1	0	0.05	0	0.0020
A3	0.152 (REF)		0.006 (REF)	
D	1.55	1.65	0.0610	0.0650
E	1.55	1.65	0.0610	0.0650
D1	0.9	1.1	0.0354	0.0433
E1	0.5	0.7	0.0197	0.0276
k	0.2 (MIN)		0.0079 (TYP)	
b	0.2	0.3	0.0079	0.0118
e	0.5 (TYP)		0.0197(TYP)	
L	0.164	0.316	0.0065	0.0124

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