

Features

Maximum output current is 1.2A

Range of operation input voltage: Max 15V

• Line regulation: 0.03%/V (typ.)

• Standby current: 2mA (typ.)

• Load regulation: 0.2%/A (typ.)

• Environment Temperature: -20°C~85°C

Applications

 Power Management for Computer Mother Board, Graphic Card

LCD Monitor and LCD TV

DVD Decode Board

ADSL Modem

Post Regulators For Switching Supplies

General Description

QN1117 is a series of low dropout three-terminal regulators with a dropout of 1.3V at 800mA load current. QN1117 features a very low standby current 2mA compared to 5mA of competitor.

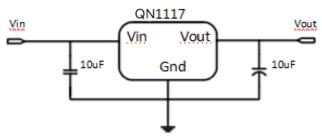
Other than a fixed version, Vout = 1.2V, 1.8V, 2.5V, 2.85V, 3.3V, and 5V, QN1117 has an adjustable version, which can provide an output voltage from 1.25 to 12V with only

two external resistors.

QN1117 offers thermal shut down function, to assure the stability of chip and power system. And it uses trimming technique to guarantee output voltage accuracy within 2%. Other output voltage accuracy can be customized on demand, such as 1%.

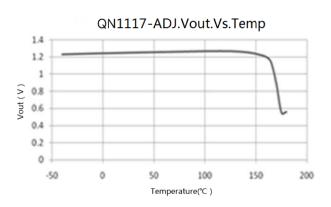
QN1117 is available in SOT-223, TO-252 power package.

Typical Application



Application circuit of QN1117 fixed version

Typical Electrical Characteristic



Selection Table

Marking	Part No.	Output Voltage	Package
	XX=12	1.2V	
	XX=18	1.8V	
1117	XX=28	2.85V	COT 222
XX YYWW	XX=25	2.5V	SOT-223 TO-252
	XX=33	3.3V	10-252
	XX=50	5.0V	
	XX=AD	Adj	

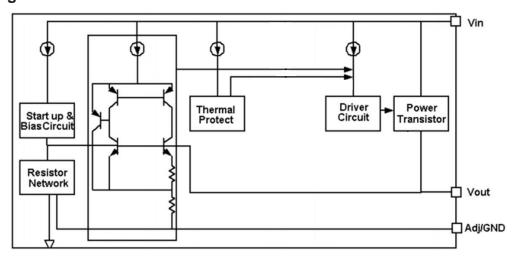
Ordering Information

Marking	Designator	Description
1117	1117 C	Product code
1117	XX	Output Voltage(1.2~12.0V)
XX YYWW	YYWW	DATE CODE

 $Note: "XX" \ stands \ for \ output \ voltages. \ Other \ voltages \ can \ be \ specially \ customized$

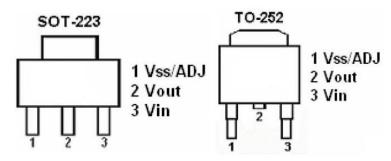
Parameters	Description	
Temperature & Rohs	C:-40~85℃, Pb Free Rohs Std.	
Package type	L:SOT-223	
	O:TO-252	
Packing type:	TR: Tape & Reel (Standard)	
Voltage accuracy	2%(Customized)	

Block Diagram





Pin Configuration



Absolute Maximum Ratings

Max Input Voltage ·····	··18V	
Max Operating Junction Temperature(Tj) · · · · · · · · · · · · · · · · · · ·	150℃	
Ambient Temperature(Ta) · · · · · · · · · · · · · · · · · · ·	40°C~	85℃
Storage Temperature(Ts)	·· -40°C~	-150°C
Lead Temperature & Time	260℃ ′	10S

Caution: Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.

Recommended Work Conditions

Recommended maximum input voltage	15V
Recommended operating junction temperature(Ti)	-20~125°C

Thermal Information

Parameter	Package	Rating	Unit
Package thermal resistance	SOT-223	20	°C/W
	TO-252	12.5	°C/W



Electrical Characteristics

Ta=25 $^{\circ}\text{C}_{+}$ unless otherwise noted.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Vref	Reference	QN1117-Adj	1.225 1.25		1.275	V
	voltage	10mA≤lout≤800mA , Vin=3.25V				
		QN1117-1.2V	1.176	1.2	1.224	V
		0≤lout≤800mA , Vin=3.2V				
		QN1117-1.8V	1.764	1.8	1.836	V
		0≤lout≤800mA , Vin=3.8V				
		QN1117 -2.5V	2.45	2.5	2.55	V
Vout	Output voltage	0≤lout≤800mA , Vin=4.5V				
		QN1117 -2.85V	2.793	2.85	2.907	V
		0≤lout≤800mA , Vin=4.85V				
		QN1117 -3.3V	3.234	3.3	3.366	V
		0≲Iout≲800mA , Vin=5.3V				
		QN1117 -5.0V	4.9	5	5.1	V
		0≲Iout≲800mA , Vin=7.0V				
		QN1117 -1.2V		0.03	0.2	%/V
		lout=10mA, 2.7V≲Vin≤10V				
	Line regulation	QN1117 -ADJ		0.03	0.2	%/V
		lout=10mA, 2.75V≲Vin≤12V				
		QN1117 -1.8V		0.03	0.2	%/V
		lout=10mA, 3.3V≲Vin≤12V				
△Vout		QN1117 -2.5V		0.03	0.2	%/V
		lout=10mA, 4.0V≲Vin≤12V				
		QN1117 -2.85V		0.03	0.2	%/V
		lout=10mA, 4.35V≲Vin≤12V				
		QN1117 -3.3V		0.03	0.2	%/V
		lout=10mA, 4.8V≲Vin≤12V				
		QN1117 -5.0V		0.03	0.2	%/V
		lout=10mA, 6.5V≲Vin≤12V				
		QN1117 -1.2V		2	8	mV
		Vin =2.7V, 10mA≤lout≤800mA		_		
		QN1117 -ADJ		2	8	mV
		Vin =2.75V, 10mA≤lout≤800mA				
		QN1117 -1.8V		3	12	mV
		Vin =3.3V, 10mA≤lout≤800mA				



△Vout	Load	QN1117 -2.5V	4	16	mV
	regulation	Vin =4.0V, 10mA≤lout≤800mA			
		QN1117 -2.85V	5	20	mV
		Vin =4.35V, 10mA≤lout≤800mA			
		QN1117 -3.3	6	24	mV
		Vin =4.8V, 10mA≤lout≤800mA			
		QN1117 -5.0	9	36	mV
		Vin =6.5V, 10mA≤lout≤800mA			
Vdrop	Dropout voltage	lout =100mA	1.2	1.3	V
		lout=800mA	1.3	1.5	V
Imin	Minimum load	QN1117 -ADJ	2	10	mA
	current				
		QN1117 -1.2V,Vin=10V	2	5	mA
		QN1117 -1.8V,Vin=12V	2	5	mA
lq	Quiescent	QN1117 -2.5V,Vin=12V	2	5	mA
	Current	QN1117 -2.85V,Vin=12V	2	5	mA
		QN1117 -3.3V,Vin=12V	2	5	mA
		QN1117 -5.0V,Vin=12V	2	5	mA
lAdj	Adjust pin	QN1117 -ADJ	55	120	uA
	current	Vin=5V,10mA≤Iout≤800mA			
Ichange	ladj change	QN1117 -ADJ	0.2	10	uA
		Vin=5V,10mA≤Iout≤800mA			
OTP	Thermal	Junction Temperature	+200		$^{\circ}$
	Shutdown				
	Thermal	Junction Temperature	+30		$^{\circ}$
	Shutdown				
	Hysteresis				
Δ V / Δ T	Temperature		±100		ppm
	coefficien				
θ JC	Thermal	SOT-223	20		
o JG	resistance	TO-252	10		°C/W

Note1: All test are conducted under ambient temperature 25 $^\circ\,$ C and within a short period of time 20ms

Note2: Load current smaller than minimum load current of QN1117-ADJ will lead to unstable or oscillation output.

Detailed Description

QN1117 is a series of low dropout voltage, three terminal regulators. Its application circuit is very simple: the fixed version only needs two capacitors and the adjustable version only needs two resistors and two capacitors to work. It is composed of some modules including start-up circuit, bias circuit, bandgap, thermal shutdown, power transistors and its driver circuit and so on.

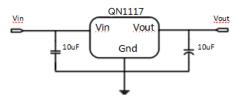
The thermal shut down modules can assure chip and its application system working safety when the junction temperature is larger than 140°C.

The bandgap module provides stable reference voltage, whose temperature coefficient is compensated by careful design considerations. The temperature coefficient is under 100 ppm/°C. And the accuracy of output voltage is guaranteed by trimming technique.

Typical Application

QN1117 has an adjustable version and six fixed versions (1.2V, 1.8V, 2.5V, 2.85V, 3.3V and 5V)

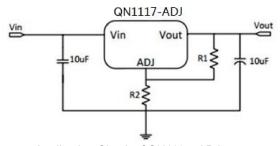
Fixed Output Voltage Version



Application circuit of QN1117 fixed version

- 1) Recommend using 10uF tan capacitor as bypass capacitor (C1) for all application circuit.
- 2) Recommend using 10uF tan capacitor to assure circuit stability.

Adjustable Output Voltage Version



Application Circuit of QN1117 -ADJ

The output voltage of adjustable version follows the equation: Vout= $1.25 \times (1+R2/R1)+IAdj \times R2$. We can ignore IAdj because IAdj (about 50uA) is much less than the current of R1 (about 2~10mA).

- 1) To meet the minimum load current (>10mA) requirement, R1 is recommended to be 125ohm or lower. As QN1117-ADJ can keep itself stable at load current about 2mA, R1 is not allowed to be higher than 625ohm.
- 2) Using a bypass capacitor (C_{ADJ}) between the ADJ pin and ground can improve ripple rejection. This bypass capacitor prevents ripple from being amplified as the output voltage is increased. The impedance of C_{ADJ} should be less than R1 to prevent ripple from being amplified. As R1 is normally in the range of $100\Omega\sim500\Omega$, the value of C_{ADJ} should satisfy this equation: $1/(2 \pi \times f_{fipple} \times C_{ADJ}) < R1$.



Thermal Considerations

We have to take heat dissipation into great consideration when output current or differential voltage of input and output voltage is large. Because in such cases, the power dissipation consumed by QN1117 is very large.

QN1117C series uses SOT-223 package type and its thermal resistance is about 20°C/W. And the copper area of application board can affect the total thermal resistance. If copper area is 5cm*5cm (two sides), the resistance is about 30°C/W. So the total

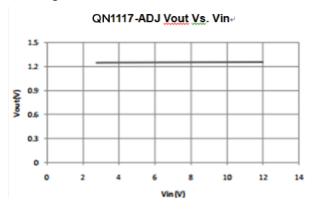
thermal resistance is about 20°C/W + 30°C/W. We can decrease total thermal resistance by increasing copper area in application board. When there is no good heat dissipation copper are in PCB, the total thermal resistance will be as high as 120°C/W, then the power dissipation of QN1117C could allow on itself is less than 1W. And furthermore,

QN1117C will work at junction temperature higher than 125°C under such condition and no lifetime is guaranteed.

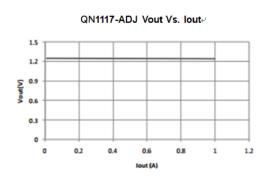
Typical Performance Charcteristics

T_A=25°C, unless otherwise noted.

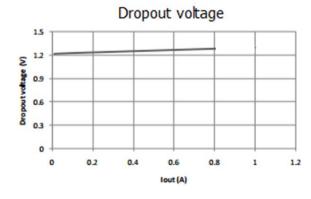
Line regulation



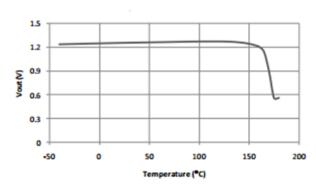
Load regulation



Dropout voltage



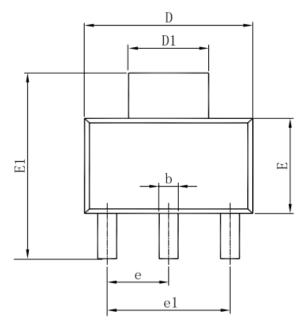
Thermal performance with OTP

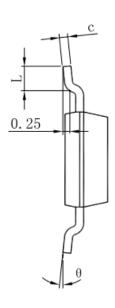




Package Information

SOT-223 PACKAGE OUTLINE DIMENSIONS





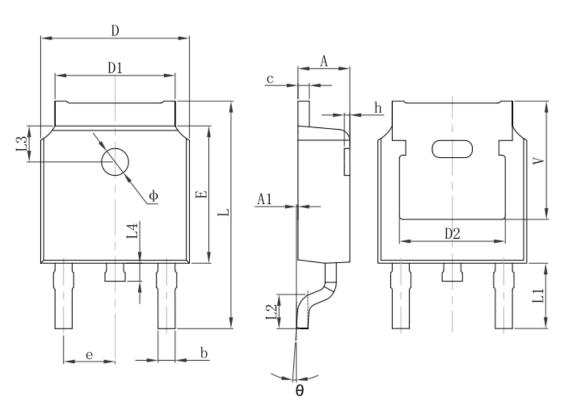




C. mb a l	Dimensions In	Millimeters	Dimensions	In Inches
Symbol	Min	Max	Min	Max
Α	1.520	1.800	0.060	0.071
A1	0.000	0.100	0.000	0.004
A2	1.500	1.700	0.059	0.067
b	0.660	0.820	0.026	0.032
С	0.250	0.350	0.010	0.014
D	6.200	6.400	0.244	0.252
D1	2.900	3.100	0.114	0.122
E	3.300	3.700	0.130	0.146
E1	6.830	7.070	0.269	0.278
е	2.300(BSC)		2.300(BSC) 0.091	
e1	4.500	4.700	0.177	0.185
L	0.900	1.150	0.035	0.045
θ	0°	10°	0°	10°



TO-252-2L PACKAGE OUTLINE DIMENSIONS



Complete al	Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
Α	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
С	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830	REF.	0.190	REF.
E	6.000	6.200	0.236	0.244
е	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 REF.		0.114	REF.
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063	REF.
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350	REF.	0.211 REF.	